

- Plurals
- Highlight all but terms initially

Aug 2004

 QRG form
 B4R form
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	U-1	Inventor	Document#	Issue #	Title	Current	Current XN	Revised	S	C	P	A	Image Doc.	P
1	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Inagawa, Hir	US 2002011	200202	Insulated gate type semiconductor device an	438/270	257/330		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 200201
2	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Noble, Wend	US 2001002	200112	Circuits and methods using vertical, comple	257/330			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 200100
3	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Noble, Wend	US 6777744	200402	Circuits and methods using vertical, comple	257/330	438/198		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 677774
4	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Madson, Gor	US 6576954	200301	Trench MOSFET formed using selective epit	438/272	257/E21.41		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 657695
5	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Furukawa, T	US 6440801	200207	Structure for folded architecture pillar mem	438/272	257/302		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 644080
6	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Noble, Wend	US 6242775	200102	Circuits and methods using vertical comple	257/330	257/351		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 624277
7	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Furukawa, T	US 6114725	200007	Structure for folded architecture pillar mem	257/330	257/296		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 611472
8	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Burns, Jr., St	US 5990509	199901	2F-square memory cell for gigabit memory ap	257/296	257/316		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 599050
9	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Park, Kyuch	US 5812559	199701	Semiconductor device having pillar shaped tr	257/316	257/301		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 581255
10	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Shah, Pradee	US 5508544	198901	Three dimensional FAMOS memory pillars	257/316	257/321		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 550854